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** CONTINUING DATA ***** THIS APPLICATION IS A DIV OF 09/814,871 03/23/2001 PAT 6,349,240				
** FOREIGN APPLICATIONS ***** JAPAN 2000-087480 03/27/2000				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/06/2002				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Verified and <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Acknowledged <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after		STATE OR COUNTRY JAPAN	SHEETS DRAWING 23	TOTAL CLAIMS 3
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TITLE Method of manufacturing semiconductor devices				
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